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| **Program - CSE** | **Semester – IV** |  |
| **Course – MBS** | **Course code - MBS160806** |  |
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| **Ch 5 – Programmable devices** | | |  |
|  | **Questions** | **Answer from** | Marks |
| Q.1 | Draw and explain architecture of 8255 PPI | PPT | 3/6 |
| Q.2 | Define the handshaking process | PPT | 3 |
| Q.3 | Draw control word register of 8255 (I/O command or BSR command) | PPT | 4 |
| Q.4 | Explain **Mode1 Handshake I/O of 8255**   1. For Input operation 2. For output operation | PPT | 3/6 |
| Q.6 | Problem statements based on 8255 Interfacing | PPT | 3/4/6 |
| Q.7 | Programs based on string instructions. | PPT | 3/4/6 |
| Q.8 | Address generation for 8255 IC (PA,PB,PC,CWR) | PPT | 3 / 4 |
| Q.9 | List any three features of 8255 | PPT | 3 |
| Q.10 | Explain 8237 process with the help of diagram | PPT | 6 |